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1 [The benefits and costs of DyC's run-time optimizations](#)


 Brian Grant, Markus Mock, Matthai Philipose, Craig Chambers, Susan J. Eggers
 September 2000 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 22 Issue 5

Publisher: ACM Press

Full text available: pdf(1.59 MB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

DyC selectively dynamically compiles programs during their execution, utilizing the run-time-computed values of variables and data structures to apply optimizations that are based on partial evaluation. The dynamic optimizations are preplanned at static compile time in order to reduce their run-time cost; we call this staging. DyC's staged optimizations include (1) an advanced binding-time analysis that supports polyvariant specialization (enabling both single-way and multi ...

Keywords: dynamic compilation, specialization

2 [Processor Sharing Queueing Models of Mixed Scheduling Disciplines for Time Shared System](#)


 L. Kleinrock, R. R. Muntz
 July 1972 **Journal of the ACM (JACM)**, Volume 19 Issue 3

Publisher: ACM Press

Full text available: pdf(1.98 MB)

 Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 [Typechecking XML views of relational databases](#)


 Noga Alon, Tova Milo, Frank Neven, Dan Suciu, Victor Vianu
 July 2003 **ACM Transactions on Computational Logic (TOCL)**, Volume 4 Issue 3

Publisher: ACM Press

Full text available: pdf(401.36 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Motivated by the need to export relational databases as XML data in the context of the Web, we investigate the *typechecking* problem for transformations of relational data into tree data (XML). The problem consists of statically verifying that the output of every transformation belongs to a given output tree language (specified for XML by a DTD), for input databases satisfying given integrity constraints. The typechecking problem is parameterized by the class of formulas defining the trans ...



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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

- ☐ 1. **Assignment of Orthologous Genes via Genome Rearrangement**
Xin Chen; Jie Zheng; Zheng Fu; Peng Nan; Yang Zhong; Lonardi, S.; Tao Jiang;
Computational Biology and Bioinformatics, IEEE/ACM Transactions on
Volume 2, Issue 4, Oct.-Dec. 2005 Page(s):302 - 315
Digital Object Identifier 10.1109/TCBB.2005.48
[AbstractPlus](#) | Full Text: [PDF](#)(2432 KB) IEEE JNL
- ☐ 2. **A debugger for Ada tasking**
Brindle, A.F.; Taylor, R.N.; Martin, D.F.;
Software Engineering, IEEE Transactions on
Volume 15, Issue 3, March 1989 Page(s):293 - 304
Digital Object Identifier 10.1109/32.21757
[AbstractPlus](#) | Full Text: [PDF](#)(1180 KB) IEEE JNL
- ☐ 3. **The Witsenhausen counterexample: a hierarchical search approach for noncc optimization problems**
Lee, J.T.; Lau, E.; Yu-Chi Ho;
Automatic Control, IEEE Transactions on
Volume 46, Issue 3, March 2001 Page(s):382 - 397
Digital Object Identifier 10.1109/9.911416
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(328 KB) IEEE JNL
- ☐ 4. **Discovering the dynamics of microbial genomes**
Dalevi, D.; Andersson, S.G.E.;
Engineering in Medicine and Biology Magazine, IEEE
Volume 20, Issue 4, July-Aug. 2001 Page(s):55 - 60
Digital Object Identifier 10.1109/51.940047
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(360 KB) IEEE JNL
- ☐ 5. **An architecture and compiler for scalable on-chip communication**
Jian Liang; Laffely, A.; Srinivasan, S.; Tessier, R.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 12, Issue 7, July 2004 Page(s):711 - 726
Digital Object Identifier 10.1109/TVLSI.2004.830919
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(1472 KB) IEEE JNL
- ☐ 6. **A Cost-Effective Main Memory Organization for Future Servers**
Ekman, M.; Stenstrom, P.;
Parallel and Distributed Processing Symposium, 2005. Proceedings. 19th IEEE Inte



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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard


- ☐ 1. Xception: a technique for the experimental evaluation of dependability in mod computers
Carreira, J.; Madeira, H.; Silva, J.G.;
Software Engineering, IEEE Transactions on
Volume 24, Issue 2, Feb. 1998 Page(s):125 - 136
Digital Object Identifier 10.1109/32.666826
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(352 KB\)](#) IEEE JNL

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WebResults 1 - 10 of about 878 for **save "breakpoint table"**. (0.42 seconds)Read Messages

... **Save** the image. This locks the No Stretch (0,0 - 255,255 for 8-bit images) **breakpoint table** to the image so when you create a new image Imagine uses this ...

lyris.dundee.net/read/messages?id=345399 - 27k - Supplemental Result - [Cached](#) - [Similar pages](#)

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Figure 3: **Save** file in a universal format such as comma-delimited (CSV) or tab-delimited ... and how to calculate letter grades from a **breakpoint table**. ...

https://www.missouri.edu/e-prof/examples/e-prof/ - 33k - [Cached](#) - [Similar pages](#)

OPT A : Generate MIKBUG Records. ** ** M68008 MONITOR PROGRAMME ...

... MOVE.L D1,D3 ; **Save** in D3 MOVEA #MSG0,A0 ; Write ' TO ' ... Z MOVEA #BPTAB,A1 ; A1 -> **Breakpoint Table**. MOVEQ #3,D3 ; 4 breakpoints. ...

www.cs.tcd.ie/Jeremy.Jones/3d2/monit.sk1 - 34k - Supplemental Result - [Cached](#) - [Similar pages](#)

BRKEDIT Manual

SAVE - saves as a text file of separate lines containing the data pair time ... this applies squares or square roots to the values in the **breakpoint table**. ...

www.bath.ac.uk/~masjpf/CDP/cbrkedit.htm - 22k - [Cached](#) - [Similar pages](#)

Unsupervised Classification in Focus | Mapping, Analysis & Design ...

... The class name can be changed by selecting the **Breakpoint table** cell and editing the cell. ... When finished, **save** the PCT segment. ...

www.fes.uwaterloo.ca/computing/services/rs/FAQ/unsupervised.html - 40k - Supplemental Result - [Cached](#) - [Similar pages](#)

RF expert page

If you modified something in the file don't forget to **save** it under a new ...

What you have to do if you have modified a **breakpoint table** is written below. ...

www.sls.psi.ch/controls/help/operators-manual/rf/expert.html - 19k - [Cached](#) - [Similar pages](#)

Online User's Guide

... is displayed. The **breakpoint table** is at address 0FE0. Address (Hex), Description, Default Value. ... Cassette **save**. Enter start address. Enter end address. ...

www.chrisbot.com/robot/hero/guide.htm - 29k - Supplemental Result - [Cached](#) - [Similar pages](#)

NAM SBUG18 MP-09 MONITOR OPT PAG PAG * MONITOR PROGRAM FOR THE ...

... PORT ECHO RMB 1 ECHO FLAG BPTBL RMB 24 **BREAKPOINT TABLE** BASE ADDR ACIAS EQU ... LIMITS PSHS X,Y **SAVE** ADDRESSES ON STACK BVS ADJSK6 EXIT IF NOT VALID HEX ...

www.swtpc.com/mholley/MP_09/sbug_src.txt - 36k - [Cached](#) - [Similar pages](#)

ttl assist05E2 february 18, 1985 * * monitor for the MC146805E2 ...

... jump op code cta equ \$01 host communications default terminator * * work ram * * org \$0010 bkptbl rmb 3*numbkp **breakpoint table** asave rmb 1 temp **save** for a ...

www.embeddedlogic.com/embarchives/68XX/assist05.asm - 33k - Supplemental Result - [Cached](#) - [Similar pages](#)

[PDF] MAXAM 1.5.doc

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Several other external commands have been added, including **LOAD** and **SAVE**, **MEM**,